

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A liquid crystal display comprising:
 - a pair of first and second signal lines transmitting select pulses having opposite polarity;
 - a third signal line transmitting data voltages;
 - ~~first and second field generating electrodes separated from each other with a gap; and~~
 - a first field generating electrode formed between the first signal line and the second signal line;
 - ~~a plurality of diodes~~ first diode connected between the first and the second signal lines and the first and the second field generating electrodes and providing at least two different resistances. electrode; and
 - a second diode connected between the second signal lines and the first field generating electrode and having a resistance different from the first diode.
2. (Currently Amended) The liquid crystal display of claim [[1]] 7, wherein the first to fourth diodes comprises MIM diodes.

3. (Currently Amended) The liquid crystal display of claim ~~[[1]]~~ 7, wherein the third signal line has at least one cutout or protrusion.

4. (Currently Amended) A diode array panel for a display device comprising:
a substrate;
a pair of first and second signal lines transmitting select pulses having opposite polarity;

~~first and second field generating electrodes separated from each other with a gap; and~~

a first field generating electrode formed between the first signal line and the second signal line;

~~a plurality of diodes~~ first diode connected between the first and the second signal lines line and the first ~~and the second~~ field generating electrodes ~~and providing at least two different resistances.~~ electrode; and

a second diode connected between the second signal line and the first field generating electrode and having a resistance different from the first diode.

5. (Currently Amended) The diode array panel of claim ~~[[4]]~~ 8, wherein the diodes comprises MIM diodes.

6. (Currently Amended) The diode array panel of claim ~~[[4]]~~ 8, wherein the MIM diodes includes first and second input electrodes connected to the first and the second signal lines, respectively, first and second insulators disposed on the first and the

second input electrodes, respectively, and first and second ~~diodes~~ output electrodes disposed on the first and the second insulators, respectively, and connected to the first and the second bottom electrodes, respectively.

7. (New) The liquid crystal display of claim 1, further comprising:

a second field generating electrode formed between the first signal line and the second signal line and separated from the first field generating electrode with a gap;

a third diode connected between the first signal line and the second field generating electrode; and

a fourth diode connected between the second signal line and the second field generating electrode and having a resistance different from the third diode,

wherein the first and the second field generating electrode are corresponding to the same third signal line transmitting data voltages.

8. (New) The diode array panel of claim 4, further comprising:

a second field generating electrode formed between the first signal line and the second signal line and separated from the first field generating electrode with a gap;

a third diode connected between the first signal line and the second field generating electrode; and

a fourth diode connected between the second signal line and the second field generating electrode and having a resistance different from the third diode,

wherein the first and the second field generating electrode are included in a pixel.

9. (New) The liquid crystal display of claim 7, wherein the first diode and the fourth diode provide substantially same resistance, and wherein the second diode and the third diode provide substantially same resistance.

10. (New) The diode array panel of claim 8, wherein the first diode and the fourth diode provide substantially same resistance, and wherein the second diode and the third diode provide substantially same resistance.